

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: TANIGUCHI, et al.

Group Art Unit: 2827

Serial No.: 09/836,182

Examiner: MITCHELL, James M.

Filed: **April 18, 2001** 

P.T.O. Confirmation No.: 7961

METHOD OF FABRICATING SEMICONDUCTOR HAVING THROUGH HOLE For. PETITION FOR EXTENSION OF TIME

Commissioner for Patents Washington, D.C. 20231

Date: February 11, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated August 13, 2002 for three months from November 13, 2002 to February 13, 2003.

Please note that a one-month extension of time was filed in the U.S. Patent and Trademark Office on November 22, 2002. Attached please find a check in the amount of \$820.00 to cover the cost of the difference for an extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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